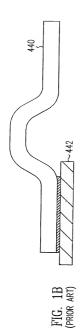
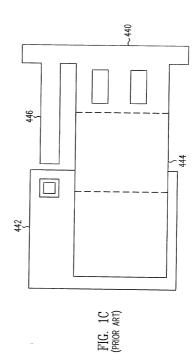


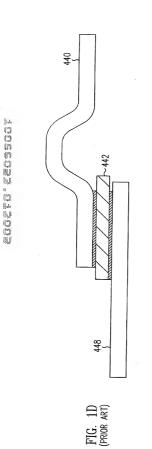
ACCOUNT DANS

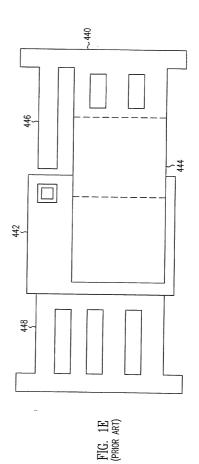
FIG. 1A (PRIOR ART)

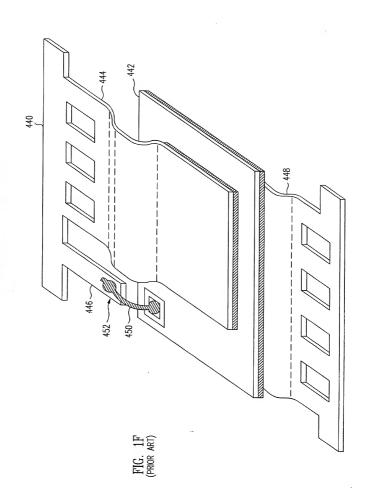












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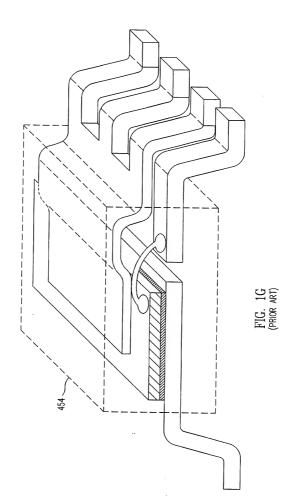
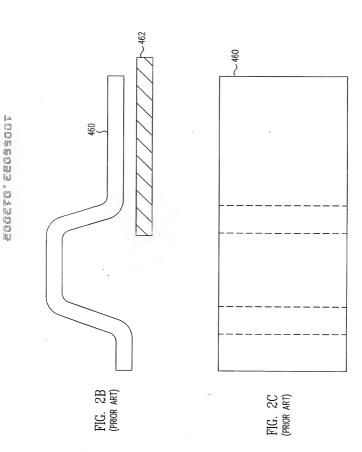
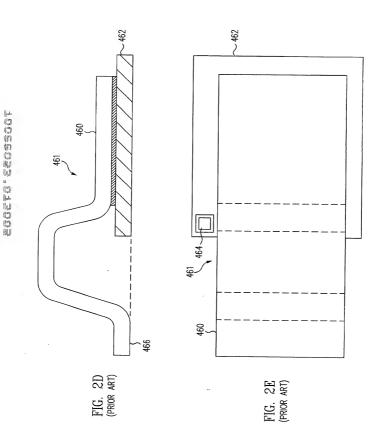
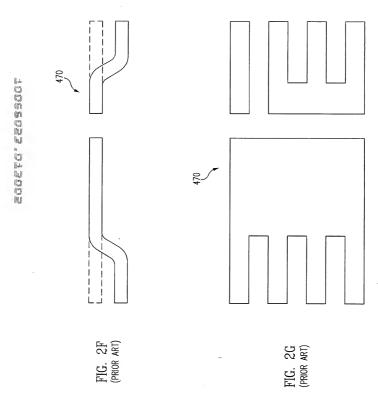


FIG. 2A (PRIOR ART)

FIG. 2R

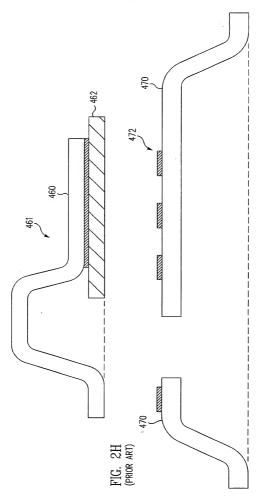




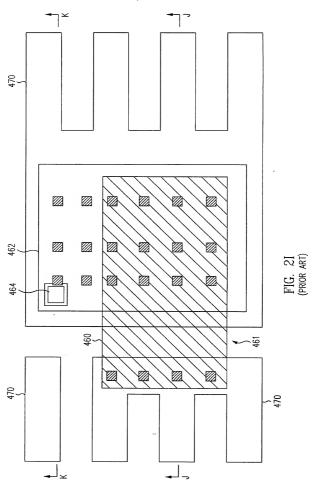


Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.









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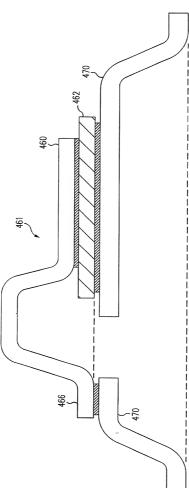


FIG. 2J (PRIOR ART)

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.



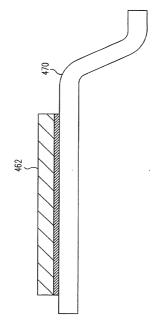
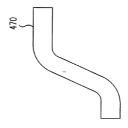


FIG. 2K (PRIOR ART)



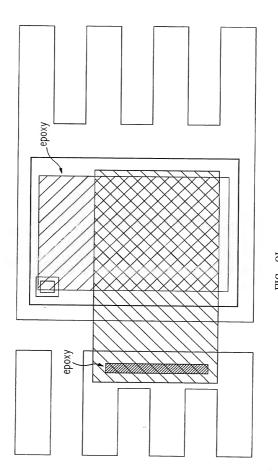


FIG. 2L (PRIOR ART)

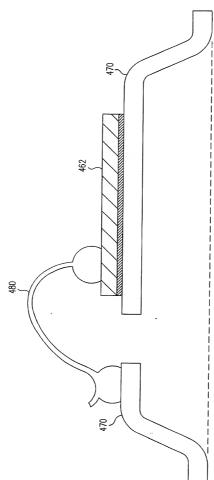


FIG. 2M (PRIOR ART)

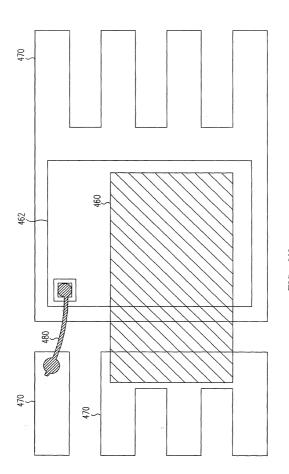


FIG. 2N (PRIOR ART)

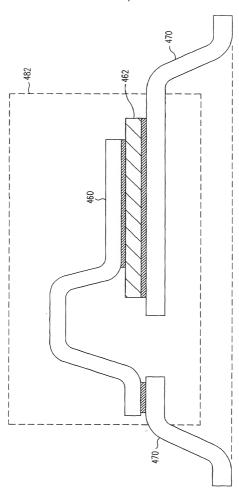
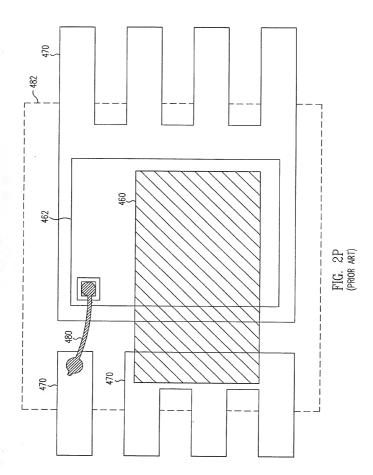
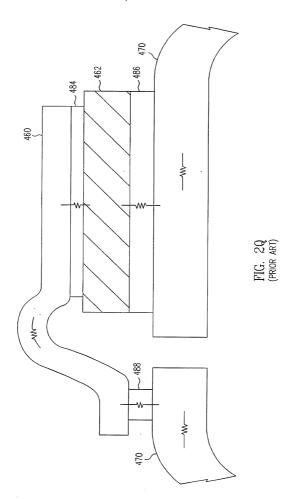
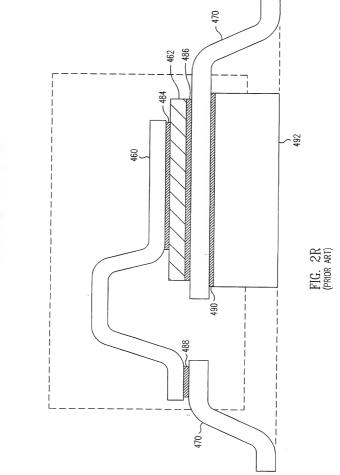


FIG. 20 (PRIOR ART)







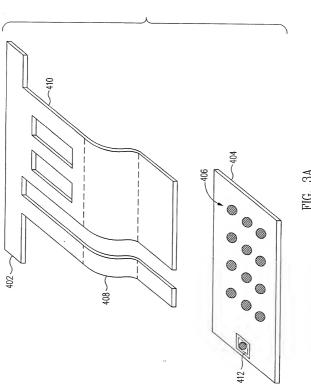


FIG. 3A (PRIOR ART)

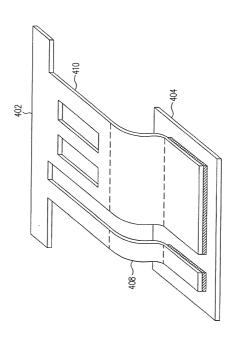


FIG. 3B (PRIOR ART)

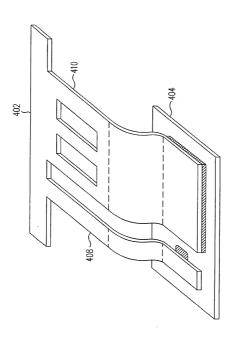
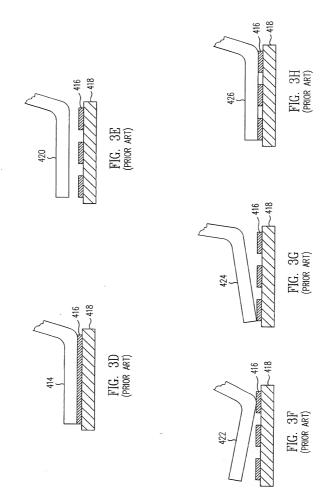
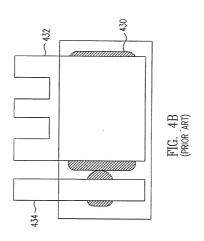
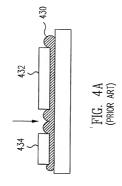


FIG. 3C (PRIOR ART)







Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL. 26/60

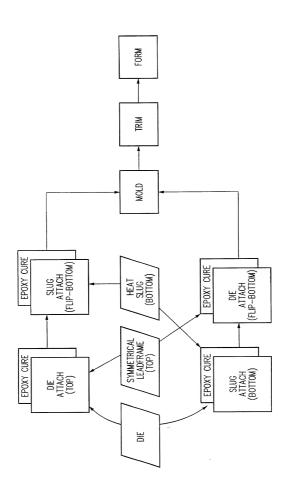
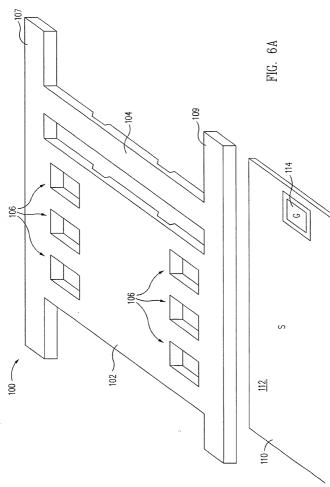


FIG. 5

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.







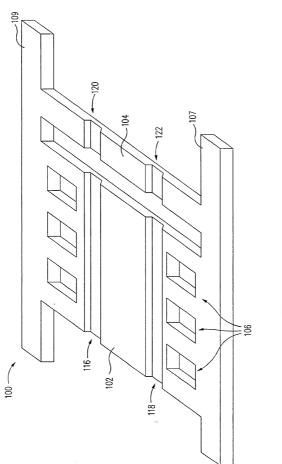


FIG. 6B

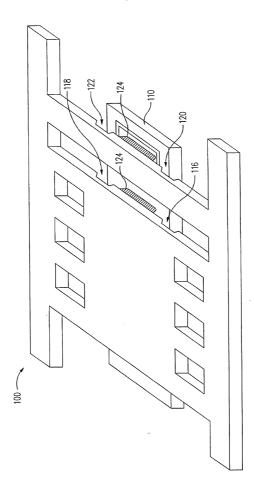
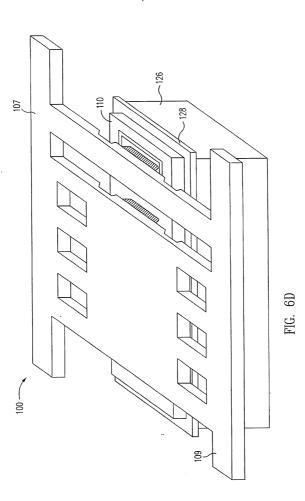
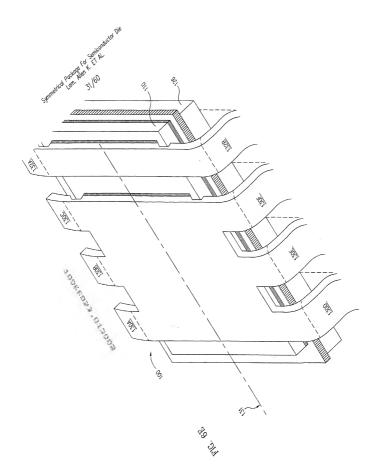
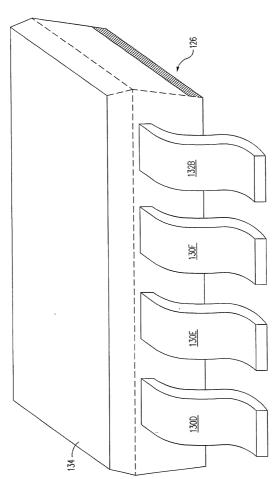


FIG. 6C

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TG. 6F

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL. 33/60

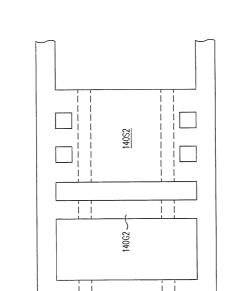


FIG. 7A

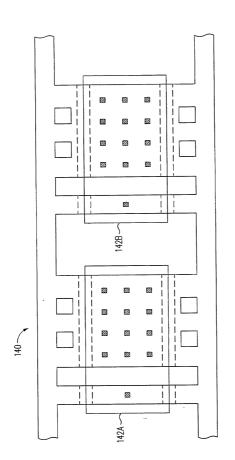
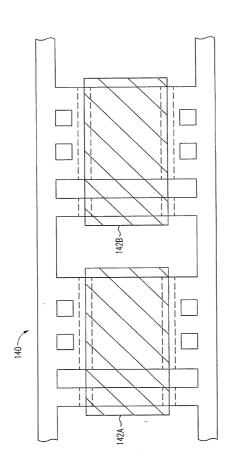


FIG. 7B

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.

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7. .JG.

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.

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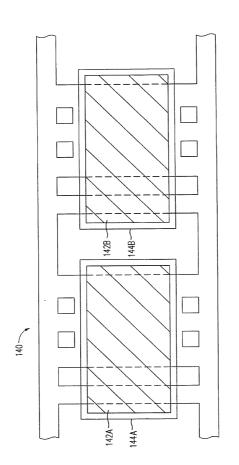


FIG. 7D

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL. 37/60

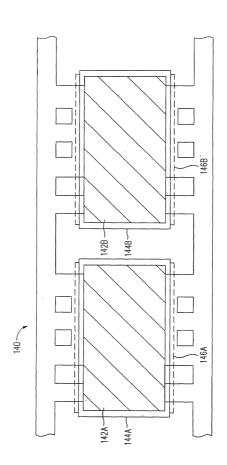


FIG. 7E

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.

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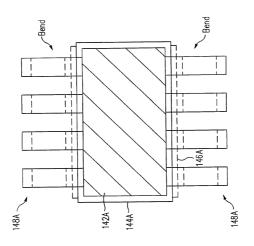
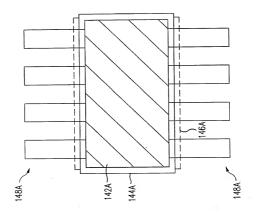


FIG. 7G

FIG. 7F



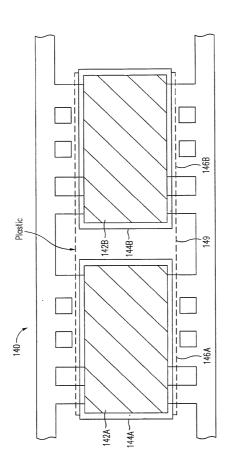
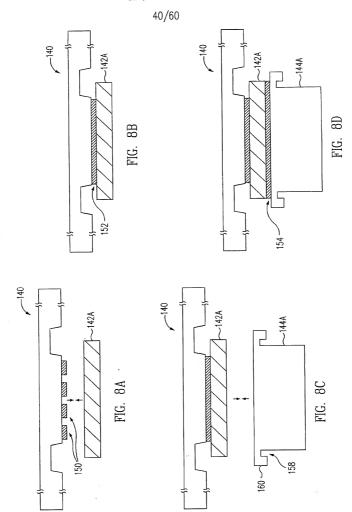


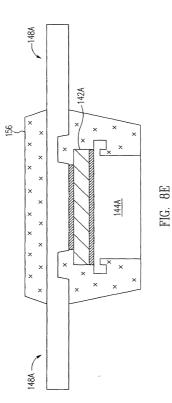
FIG. 7H

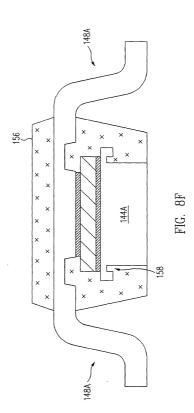
Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.



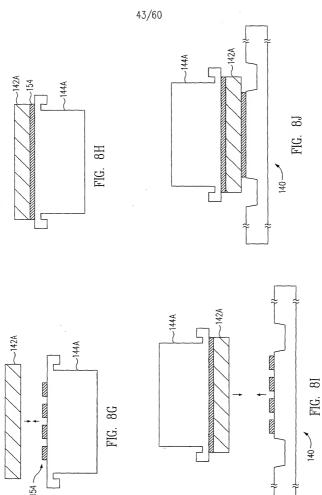
Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.

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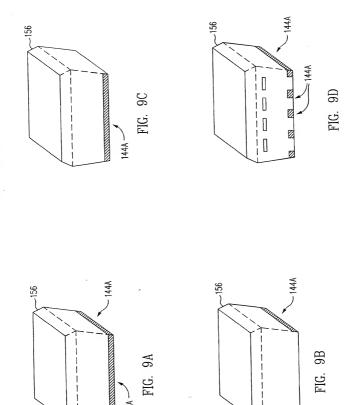




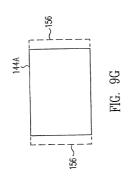
Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.

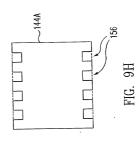


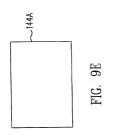
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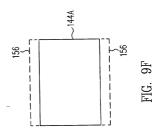


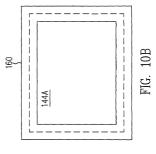


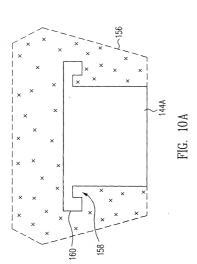


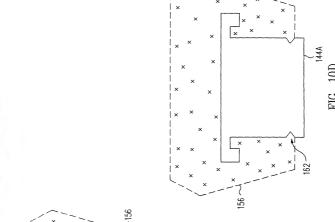


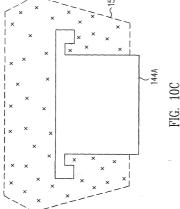


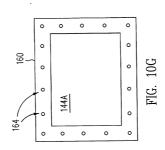


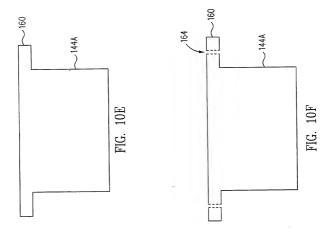


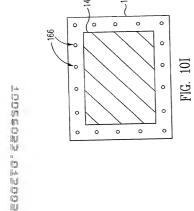


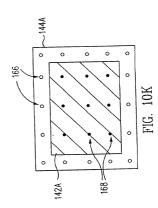


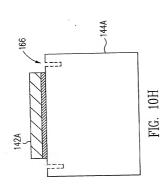


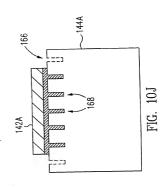












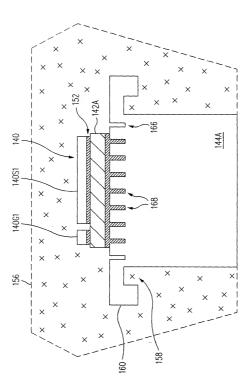


FIG. 10L

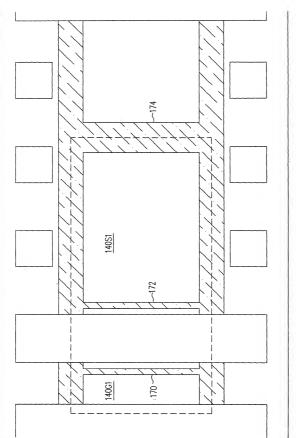
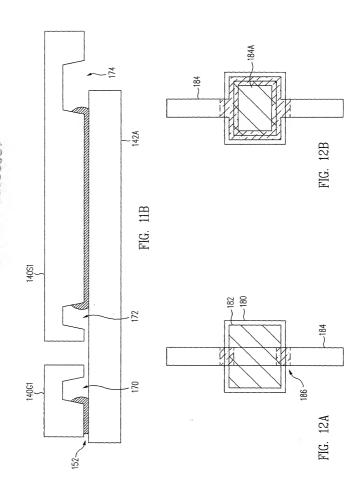


FIG. 11A





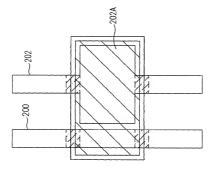


FIG. 12D

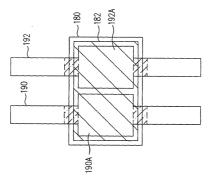
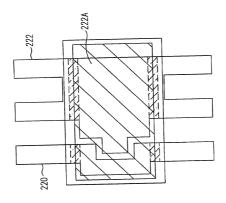
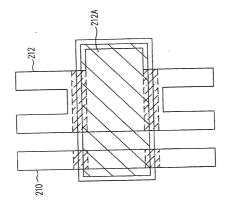


FIG. 12(

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL. $54 \overline{/60}$





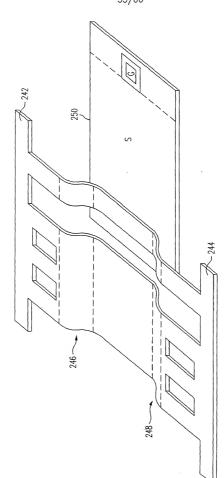


FIG. 13A

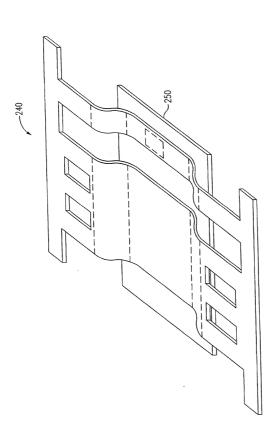
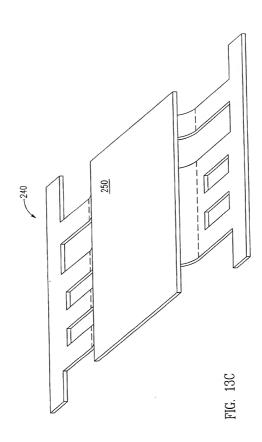
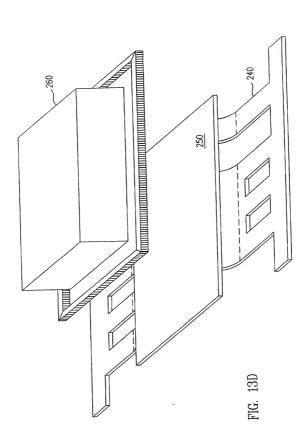


FIG. 13B

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.

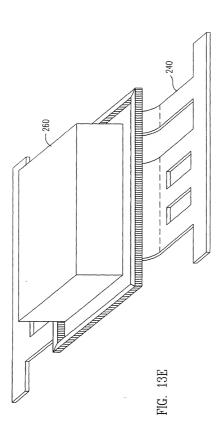
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Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.





Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.

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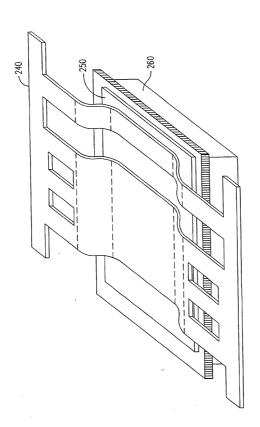


FIG. 13F